

AMENDMENTS TO THE SPECIFICATION

[0034] Transmit section 405 is largely conventional, but is modified in accordance with one embodiment to allow for one-time, periodic, or continuous variation in the timing of the transmit clock TxClk. Transmit section 405 conventionally includes a resynchronizer 420 that re-times parallel transmit data TxData timed to a local clock LClk to transmit clock TxClk. The resulting re-timed parallel data TxDr is then fed to a serializer 423. Serial transmit data TxDs from serializer 423 is then conveyed to a transmitter 426 for transmission over channel 402. In one embodiment, resynchronizer 420 is of a type described in U.S. Patent Application Serial No. 10/282,531, which issued on September 27, 2005, as U.S. Patent No. 6,949,958, entitled “Method and Apparatus for Fail-Safe Resynchronization with Minimum Latency,” which is incorporated herein by reference.

[0041] As noted above in connection with Figure 1, modern high-speed communication systems often include a complex matrix of densely populated communication channels. Some or all of the aggressor and victim transmitters may be provided with phase-adjustment circuits that can be optimized for a given noise environment. It may be sufficient, for example, to include phase adjustment for only [[of]] a subset of potential aggressors and victims.